

<b>Notice of References Cited</b>	Application/Control No. 10/779,379		Applicant(s)/Patent Under Reexamination CHAO, IWEN	
	Examiner Anh D. Mai		Art Unit 2814	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-4,160,990	07-1979	Kane, Jeffrey	257/491
*	B	US-4,408,304	10-1983	Nishizawa et al.	365/174
*	C	US-5,015,594	05-1991	Chu et al.	438/207
*	D	US-6,169,007	01-2001	Pinter, Jerald Frank	438/320
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	G. Zimmer et al. A fully Implanted NMOS, CMOS, Bipolar Technology for VLSI and Analog-Digital Systems. IEEE 1979, pp. 312-318.
	V	H. Momose et al. 1.0micron n-Well CMOS/Bipolar Technology. IEEE 1985, pp. 217-223.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.